

**ROM Mask / Hard Mask certificate**  
**CERTIFICATE**

It is certified that the ROM Mask (part # \_\_\_\_\_)  
under which the Microcontroller chips have been manufactured, samples of  
which are submitted by M/s (name of the applicant) for SCOSTA certification to  
NIC under Registration No. \_\_\_\_\_ are owned by (name of mask  
owner) who have the exclusive authority for this mask to be used for production  
of Microcontroller chips.

Authorized signatory of  
  
chip manufacturer