

# Syllabus for Nanoelectronics PGQP08 (viii)

## Unit-I

Electronic Transport in semiconductor, PN Junction, Diode equation and diode equivalent circuit. Breakdown in diodes, Zener diodes, Tunnel diode, Semiconductor diodes, characteristics and equivalent circuits of BJT, JFET, MOSFET, IC fabrication-crystal growth, doping, bonding, Thin film active and passive devices. Rectifiers, Voltage regulated ICs and regulated power supply, Biasing of Bipolar junction transistors and JFET. Single stage amplifiers, Multi stage amplifiers, Feedback in amplifiers, oscillators, function generators, multi vibrators, Operational Amplifiers (OP AMP): Characteristics and Applications, Computational Applications, Integrator, Differentiator.

## Unit-II

Network theorems, Network graphs, Nodal and Mesh analysis. Time and frequency domain responses. Image impedance and passive filters. Two-port Network Parameters. Transfer functions, Signal representation. State variable method of circuit analysis, AC circuit analysis, Transient analysis. Logic families, flip-flops, Gates, Boolean algebra and minimization techniques, Multi vibrators and clock circuits, Counters-Ring, Ripple. Synchronous, Asynchronous, Up and down shift registers, multiplexers and demultiplexers, Arithmetic circuits, Memories, A/D and D/A converters. Modulation index, frequency spectrum, generation of AM (balanced modulator, collector modulator), Amplitude Demodulation (diode detector Other forms of AM: Double side band suppressed carrier, DSBSC generation (balanced modulator), Single side band suppressed carrier, SSBSC generation and Phase modulation, modulation index.

## Unit-III

Electrostatics: Gauss's law and its applications, Laplace and Poisson equations, boundary value problems. Magneto statics: Biot-Savart law, Ampere's theorem. Electromagnetic induction. Maxwell equations. Reflection and refraction, polarization.

## Unit-IV

Microprocessor: Introduction to 8085, Basic Concepts of Microprocessors, Central Processing Unit: CPU, I/O devices, clock, memory, bussed architecture, tristate logic, address bus, data bus and control bus. Development of semiconductor memory, internal structure and decoding, memory read and write timing diagrams, MROM, ROM, EPROM, EEPROM, DRAM: Intel 8085 microprocessor.

